

Missing PVH bits

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- ▶ HVM domain without a (mandatory) QEMU device model.



- ▶ 2013: PVHv1 domU support merged
- ▶ 2014: PVHv1 dom0 support merged
- ▶ 2016: PVHv2 domU introduction
- ▶ 2017: remove PVHv1
- ▶ 2018: PVHv2 dom0 introduction



- ▶ Do not use a different domain type internally in the hypervisor: PVHv2 is an HVM domain without any ioreq server.
- ▶ Do not blindly propagate PV interfaces into PVHv2.
- ▶ Consider what is required in order to make use of the hardware provided assistances: vAPIC or posted interrupts.
- ▶ Avoid introducing (Xen) PV specific interfaces when possible.
- ▶ New PVHv2 specific entry point ABI.



- ▶ dom0 requires access to physical devices, and for HVM guests that involves a device model (QEMU).
- ▶ On PV some interactions with devices involve using hypercalls.
- ▶ In order to allow a mostly transparent interaction with devices PCI config space emulation is required.
- ▶ Legacy PCI interrupts also require an IO-APIC.
- ▶ Identifying device MMIO regions is hard.



- ▶ UEFI (OVMF) firmware.
- ▶ PCI device passthrough.



- ▶ Wider testing.
- ▶ Some PCI capabilities won't work: Resizable BARs, SR-IOV (?).
- ▶ NMI handling: `nmi=dom0` command line option not implemented.
- ▶ MCA support.
- ▶ Lack of PCI passthrough support.
- ▶ Linux: support for C and P state reporting.
- ▶ Physical CPU hotplug.
- ▶ Slowness of hypercalls on HVM.
- ▶ Security support.



- ▶ PVH domU is overall in a better position.
- ▶ PVH dom0 needs more work, and parties interested in using it.



Thanks
Questions?